Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7878	(buffer adj amplifier) and parallel	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/01/12 10:29
L2	3226	1 and delay	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/01/12 10:29
L3	990	2 and (output adj buffer)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/01/12 10:29
L4	12	3 and (adjust$3 adj delay)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/01/12 10:31
L5	176	3 and (delay adj time)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/01/12 10:31
L6	78	5 and semiconductor	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/01/12 10:32
L7	15	6 and DRAM	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/01/12 10:35
L8	1	3 and (adjustable adj delay adj time)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/01/12 10:35